# Ji Chenyang
## My major:Electronic   Engineering

**Date of Birth**：2002-02-06     **Phone**：18568101691

**Politics Status**：Community Youth League Member

**Email**：jichy@shanghaitech.edu.cn

## Information

**Will School**：ShanghaiTech University

**Apply for Major**：Electronics Science and Technology

**Score**：English：66      Math：67     Politics：71     Circuit Theory：123     Total Score：327

## Education

**Graduate School**：ShanghaiTech University     **Expected Graduation Date**：2020.09-2024.06
**Major**：Electronic Engineering

**Major Couse**：Fundamentals of Circuits, Digital Circuits, Analog Circuits, Computer Programming,

Electromagnetics, Signals and Systems, Control Principles, Fundamentals of Semiconductor

## Campus Experience

**Social Practice(Enshi,HuBei) (2021.08-2021.09)    Responsible For**: writing press releases for the

team and publishing them on wechat official account.

**Industry Practice(Integrated Circuit)  (2022.08-2022.09) Responsible for:** taking meeting

minutes and sorting out relevant materials.

## Skills

CET-4,CET-6
Good knowledge of c/ C ++, python, verilog and other programming languages
Proficient in wps, matlab and other tools

## Self-Evaluation

Honest and trustworthy, careful and meticulous, responsible, with enterprising spirit, realistic attitude and self-management ability

Strong self-learning ability, and can actively and efficiently learn new technical knowledge, good documentation skills

Be able to communicate and collaborate well with others and have a team spirit